Américo **Dias**
Sr. Analog Design Engineer

T. +45 91 25 89 58   **W.** https://americodias.com   **M.** americo.dias@gmail.com   **A.** Copenhagen, Denmark

PROFILE

I have 16+ years of experience in the electronics industry. From technician to analog design engineer in the most advanced technologies in the world, working on both small companies and worldwide big corporates.

EDUCATION

SET 1997 – AUG 2000
Technical Diploma
Colégio de Gaia: Electronics Technician Level III

SET 2000 – AUG 2010
BSc. and MSc. Electrical and Computers Eng.
Universidade do Porto: The master thesis was the development of a ultra–wideband (UWB) transmitter for biomedical implants in 90nm CMOS process. Thesis score was 19/20.

SET 2010 – SET 2011
PhD. Candidate
Universidade do Porto: Completed the 1st year composed by 8 courses with a GPA of 16/20. Put on hold to pursue a career in the industry.
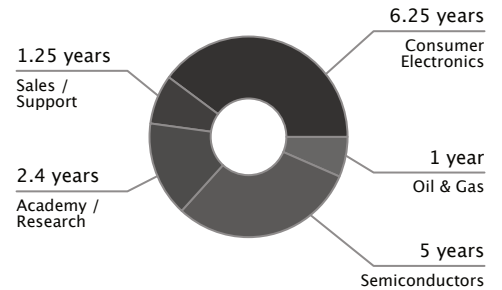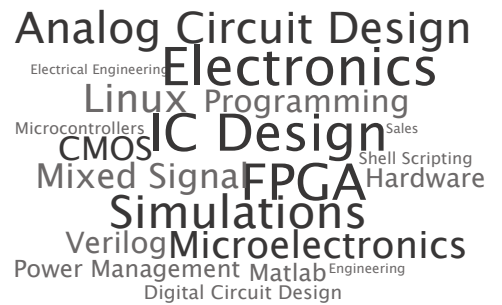
INDUSTRIES

- 6.25 years — Consumer Electronics
- 1.25 years — Sales / Support
- 2.4 years — Academy / Research
- 1 year — Oil & Gas
- 5 years — Semiconductors

STRENGTHS

Analog Circuit Design · Electrical Engineering · Electronics · Linux · Programming · Microcontrollers · Sales · CMOS · IC Design · Shell Scripting · Mixed Signal · FPGA · Hardware · Simulations · Verilog · Microelectronics · Power Management · Matlab · Engineering · Digital Circuit Design

WORK EXPERIENCE

SET 2001 – APR 2007
Electronics Technician
Corsisa Electrónica: Development of hardware/firmware in optoelectronics and testing areas. Technical support and quality control.

MAY 2007 – AUG 2008
Country Sales Representative
Venco Electrónica / RC Microelectrónica: Responsible for sales and support to all Portuguese market.

SEP 2008 – APR 2009
Firmware developer
Tellus Mater: Responsible for the firmware development for telemetry, telemedicine and security systems.

APR 2009 – SEP 2011
Researcher
INESC Porto/Universidade do Porto: Design of a capacitive readout circuit in 0,35um CMOS process and digital signal processing using Xilinx Virtex 5 FPGA. Preparation for the Doctoral Programme. Worked as adjunct professor responsible for practical classes.

JUN 2011 – DEC 2012
Physical Design Engineer
Synopsys: As part of the physical design team, my main tasks included synthesis, P&R, floorplaning, timing closure, electrical and physical verifications, among other.

JAN 2013 – MAR 2016
Analog IC Design Engineer
SiliconGate: Full custom design of power management IPs. Development of several LDOs with fast response and very high PSRR. Budget time, manage schedule and complete complex IC design tasks on time.

APR 2016 – OCT 2017
Senior Analog Design Engineer
Emerson Automation Solutions: Analysis, modeling and simulation of permanent downhole monitoring systems and devices using tools such as Matlab, Finite Element Method simulations and spice.

NOV 2017 – CURRENT
Senior Analog IC Design Engineer
Sigma Designs: As part of the ASIC team, my main responsibilities are to create specifications, supervise and design blocks for Z–Wave IoT integrated circuits.

AWARDS

'Fastest full custom layout designer of the year', EMEA region 2009, Cadence Design Systems, Inc.
Press release url: https://goo.gl/IZ35eM

REFERENCES

"The quality of the work done by Américo in that course was far above all the previous best students."
Former Professor
Vice–Rector, University of Porto

"Américo is reliable, dependable, high capacity to solve problems, persistent and good communicator."
Former Manager
R&D Manager, Synopsys Inc.

LANGUAGES

- PORTUGUESE
- ENGLISH
- SPANISH